

**REPLY UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2188**

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3/1/07

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application. No: 10/783,960

Filed: February 20, 2004

Inventor(s):  
Petersen, Thomas A.  
Vishin, Sanjay

Title: METHOD AND APPARATUS FOR GLOBAL ORDERING TO INSURE LATENCY INDEPENDENT COHERENCE

[illegible]

Examiner: Portka, Gary J.  
Group/Art Unit: 2188  
Atty. Dkt. No: MIPS.0187-02-US

**RESPONSE TO FINAL OFFICE ACTION OF  
DECEMBER 20, 2006**

**Mail Stop AF**  
**Commissioner for Patents**  
**P.O. Box 1450**  
**Alexandria, VA 22313-1450**

Dear Sir:

This paper is submitted in response to the Final Office Action of December 20, 2006, to further highlight why the application is in condition for allowance.